

Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of

Complete if Known					
Application Number	09/881934				
Filing Date	06/14/01				
First Named Inventor	ENRIQUE MUSOLL				
Group Art Unit	2188				
Examiner Name	KEVIN L. ELLIS				
Attorney Docket Number	MIPS.0173-00-US				

			U.S. PATENT	DOCUMENTS	
· Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
', Initials*	No.1	Number-Kind Code <sup>2(il known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
NR		US-5784649	07-21-1998	BEGUR ET AL.	
1		US-6529515	03-04-2003	RAZ ET AL.	
		US-6640248	10-28-2003	JORGENSEN, JACOB W.	
		US-6625808	09-23-2003	TARDITI, DAVID R.	
		US-6738371	05-18-2004	AYRES, LAWRENCE	
		US-6738378	5-18-2004	TUCK, III; RUSSELL R.	
UR		US-6813268	11-02-2004	KALKUNTE ET AL.	
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	FOREIGN PATENT DOCUMENTS								
Examiner Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	76				
Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (* known)		Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear				
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Examiner Signature	Kevin L. Ellis	Date Considered	3/16/05

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Sheet	1	of	2	Attorney Docket Number	MIPS.0173-00-US		

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Initials*	No.1	Number-Kind Code <sup>2(il known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear		
UR		US-5708814	01-13-1998	SHORT ET AL.			
	•	US-5724565	03-13-1998	DUBEY ET AL.			
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		US-6460105	10-01-2002	JONES ET AL.			
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_		US-20020049964	04-25-2002	TAKEYAMA ET AL.			
UR		US-20020071393	06-13-2002	ENRIQUE MUSOLL			
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	FOREIGN PATENT DOCUMENTS							
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Initials*		Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T				
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Examiner Signature	Kevin L. Ellis	Date Considered 3/16/05	

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Sheet	2	of	2	Attorney Docket Number	MIPS.0173-00-US

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
nR	ВА	SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
UR	BB	MELVIN ET AL, "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
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Initials*		Number-Kind Code <sup>2(# known)</sup>			Relevant Passages or Relevant Figures Appear		
NR		US-4200927	04-29-1980	Hughes et al.	·		
UR		US-6535905	03-18-2003	Kalafatis et al.			
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Initials* No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (* known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T⁰			
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Substitution Complete if Known om 1449A/PTO **Application Number** 09/881934 INFORMATION DISCLOSURE **Filing Date** 02/13/04 First Named Inventor **ENRIQUE MUSOLL** STATEMENT BY APPLICANT 2188 **Group Art Unit** (use as many sheets as necessary **Examiner Name KEVIN L. ELLIS** MIPS.0173-00-US **Attorney Docket Number** Sheet 3

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*			T²
YAMAMOTO, WAYNE. An Analysis of Multistreamed, Superscalar Processor Architectures. University of California Barbara Dissertation. December 1995. Santa Barbara, US.			
1	AB	YAMAMOTO et al. "Increasing Superscalar Performance Through Multistreaming." Parallel Architectures and Compilation Techniques (PACT '95). 1995	
	AC	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 <sup>nd</sup> Ed. May 1994. pp. 70-72. Morgan . Kaufmann. San Francisco, US.	
	AD	MC68020 32-Bit Microprocessor User's Manual. 3rd Ed 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
	AE	POTEL, M. J. "Real-Time Playback in Animation Systems." Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques. 1977. pp. 72-77. San Jose, CA, US.	
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Examiner Signature	Kas La FILS	Date 2/1/6
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un		YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.	
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	AQ	DONALDSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26 <sup>th</sup> Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.	
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	As	MUSOLL ET AL., "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core," April 16, 2001, Disclosure Document #492431, USPTO	
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R	AW	MUSOLL ET AL., "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory," July 3, 2001, Disclosure Document #496391, USPTO	
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Examiner Signature	Kevin L. Ellis	Date Considered	3/16/05
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